



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Michael B. Ball

Serial No.: 09/943,880

Filed: August 30, 2001

For: METHOD OF FABRICATION OF
STACKED SEMICONDUCTOR DEVICES

Confirmation No.: 2391

Examiner: A. Chambliss

Group Art Unit: 2827

Attorney Docket No.: 2269-2769.6US

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231.

January 7, 2003
Date

Signature

Deidra Pfeil
Name (Type/Print)

LETTER TO THE CHIEF DRAFTSMAN

Commissioner for Patents
Washington, D.C. 20231

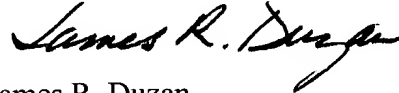
Sir:

Attached please find a copy of original drawing figure 7 with red ink markings, showing the proposed changes to the drawings in this application, for which the approval of the Examiner is requested.

Serial No. 09/943,880

No new matter has been added. Approval of the proposed revisions is respectfully requested.

Respectfully submitted,



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Date: January 7, 2003
JRD/djp

Enclosures: Drawings with changes marked in red
Document in ProLaw

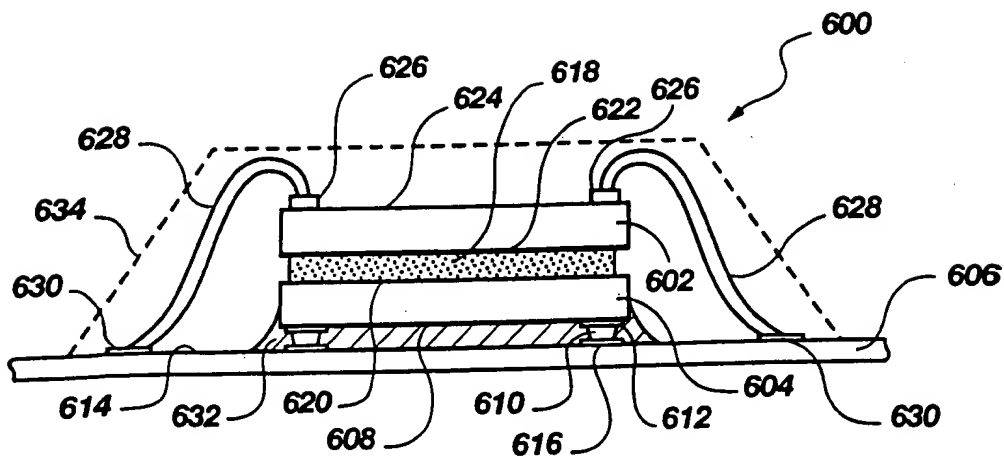


Fig. 6

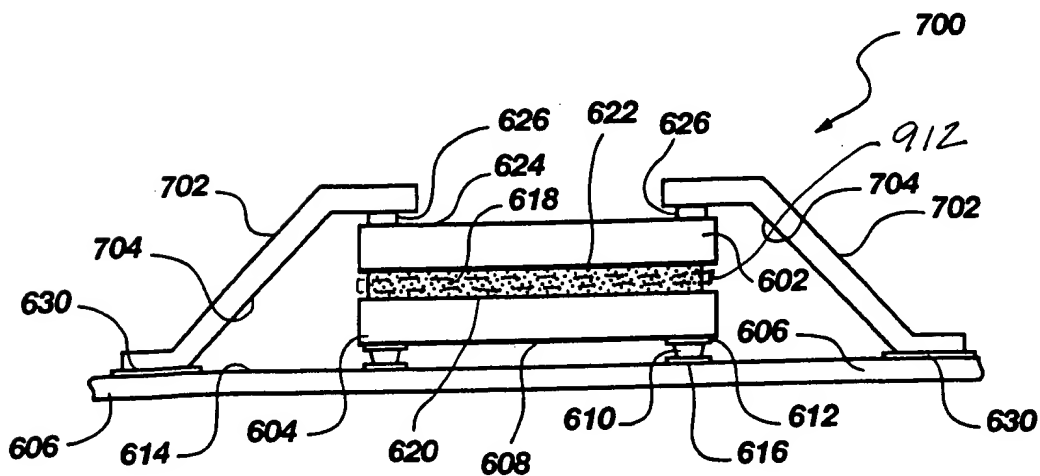


Fig. 7